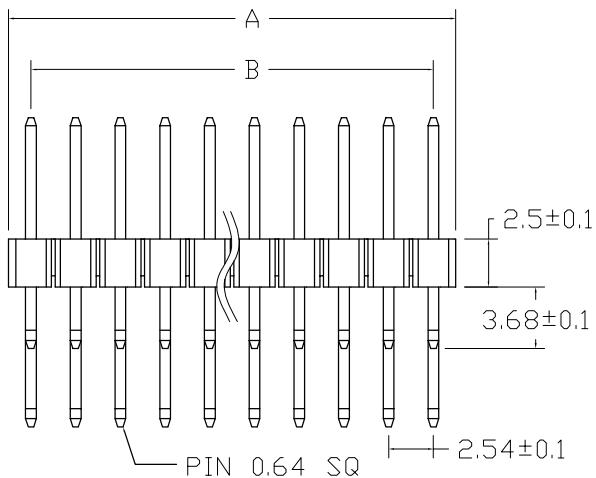
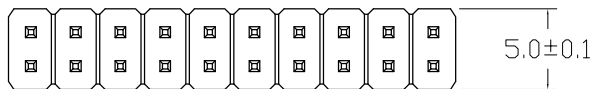


PCB LAYOUT

SPECIFICATIONS

Material:

Insulator:Glass filled PBT (UL94V-0),Black

Contacts:Brass

Plating:Au or Sn over 50u'' Ni

Electrical Charactristics:

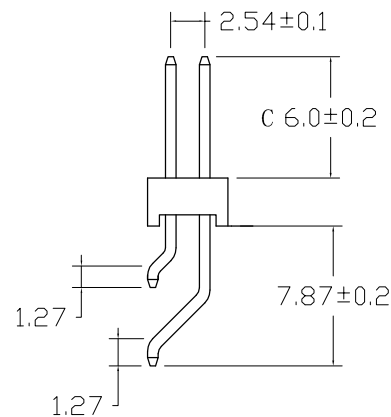
Contact Resistance:20mΩ Max.

Insulator Resistance:1000MΩ Min.

Withstanding Voltage:AC 500V

Currrnt Rating:3 AMP

Operating Temperature:-40℃ to +105℃



Position Dimension	1 Thru 40
A± 0.30	2.54 * No. of Positions
B± 0.20	2.54 * No. of Contacts-2.54

未注公差按公差分段要求

MATERIAL(材质)		单位 MM	比例 按图纸缩放	日期 2013-06-15	深圳市百富特电子有限公司	
主体	PBT					
端子	黄铜镀金					
公差 分段	>10	±0.30	制图	审查	核准	品名
	<10	±0.20				
	<3	±0.10				
视角		刘建				料号